

GENERIC SCHEDULE OF TEST RESULTS

DB reference no Location Z _s at DB (Ω) I _{pf} at DB (kA) Correct supply polarity confirmed <input type="checkbox"/> Phase sequence confirmed (where appropriate) <input type="checkbox"/>	Details of test instruments used (state serial and/or asset numbers) Continuity Insulation resistance Earth fault loop impedance RCD Earth electrode resistance
Details of circuits and/or installed equipment vulnerable to damage when testing	

Tested by:

Name (Capitals)

Signature Date

Circuit details		Test results																									
		Protective device					Conductor details																				
Circuit number	Circuit Description	2	3	4	5	6	7	8	9	10	11	Ring final circuit continuity (Ω)		Continuity (Ω) (R ₁ + R ₂) or R ₂		Insulation Resistance Test Voltage		Insulation Resistance (MΩ)		Polarity	Z _s (Ω)	RCD		AFDD	Remarks (continue on a separate sheet if necessary)		
		BS (EN)	type	rating (A)	breaking capacity (kA)	RCD I _{Δn} (mA)	Maximum permitted Z _s (Ω)*	Reference Method	Live (mm ²)	cpc (mm ²)	r ₁ (line)	r _n (neutral)	r ₂ (cpc)	(R ₁ + R ₂)	R ₂	V	Live - Live	Live - Earth	Disconnection time (ms)	RCD test button operation	Manual AFDD test button operation						
1																											
25																											

* Where the maximum permitted earth loop impedance value stated in column 8 is taken from a source other than the tabulated values given in Chapter 41 of this Standard, state the source of the data in the appropriate cell for the circuit in the 'Remarks' column (column 25) of the schedule.